

APPENDIX B. BIOGRAPHIES OF PANEL MEMBERS

Name: Dr. William R. Boulton (Panel Chair)
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Dr. Boulton is the C.G. Mills Professor of Strategic Management in the College of Business at Auburn University. His research is focused on Global, Technology-based Competition. Dr. Boulton was Visiting Professor at Germany's premier executive development institute, Universitätsseminar der Wirtschaft at Schloss Gracht, in Erfstadt, Germany during 1999. He was a Visiting Scholar at the prestigious Institute for Fiscal and Financial Policy of Japan's Ministry of Finance in 1993. He has also been Visiting Professor at Keio Business School in Japan in 1992 and 1993, where he was also hosted as a Fulbright Research Scholar in 1986.

Dr. Boulton is the chair of a new Asian Electronic Manufacturing Update study beginning in September 1999. Since 1992, he has conducted three research studies for U.S. government agencies on electronic manufacturing technologies and industrial development policies of Asian countries. The studies include Electronic Manufacturing and Packaging in Japan (1995), and Semiconductor and Electronic Manufacturing in the Pacific Rim (1997), and Information Technologies in the Development Strategies of Asia (1999).

Dr. Boulton is author of numerous articles and cases covering topics of competitive strategy, technology and innovation management, boards of directors, and strategic planning. He developed The Resource Guide for the Management of Innovation and Technology for the AACSB in 1993, and wrote a case and textbook, Business Policy: The Art of Strategic Management in 1984.

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David A. Hodges is a Professor in the Graduate School and the Daniel M. Tellep Distinguished Professor Emeritus at the University of California at Berkeley. He earned the B.E.E. degree at Cornell University in 1960 and the M.S. and Ph.D. degrees from Berkeley in 1961 and 1966. From 1966 to 1970 he worked at Bell Telephone Laboratories at Murray Hill and Holmdel, N.J. Since 1970 he has been a member of the faculty in Electrical Engineering and Computer Sciences at UC Berkeley. Following a year as Chair of the EECS Department, he served as Dean of Berkeley's College of Engineering from July 1990 through June 1996. He was appointed Professor in the Graduate School after retiring from regular teaching duties in July 1998.

From 1970-90 Professor Hodges was active in teaching and research on microelectronics technology and design. With colleagues he developed Berkeley's electrical engineering courses on bipolar and MOS digital integrated circuits. With H. G. Jackson, he co-authored the McGraw-Hill textbook *Analysis and Design of Digital Integrated Circuits*, which has been used by more than 50,000 students in two English language and several translated editions. He is an author and/or editor for two IEEE Press books, and author or co-author

of more than 130 contributions to IEEE and other refereed archival publications, including several conference papers that won “best paper” awards.

In the 1960s and 1970s, Professor Hodges was active in research on semiconductor memories, signal processing circuits, and MOS data converters and filters. Since 1985 his research has centered on semiconductor manufacturing systems. He has supervised 27 completed doctoral dissertations and 90 completed master’s degrees. Since 1991, he and Professor R. C. Leachman have led Berkeley’s interdisciplinary research program on Competitive Semiconductor Manufacturing, the first U.S. academic program of its kind.

Professor Hodges was the founding Editor of the *IEEE Transactions on Semiconductor Manufacturing*, a past Editor of the *IEEE Journal of Solid-State Circuits*, and a past General Chairman of the IEEE International Solid-State Circuits Conference. With Professors R. W. Brodersen and P. R. Gray, he received the 1983 IEEE Morris N. Liebmann Award for pioneering work on switched-capacitor circuits. He was the winner of the 1997 IEEE Education Medal and the 1999 ASEE Benjamin Garver Lamme Award. Professor Hodges is a Fellow of the IEEE and of the American Association for the Advancement of Science, and a Member of the U.S. National Academy of Engineering. He serves as a Director of Mentor Graphics Corporation, Silicon Image, Inc., and as a Trustee of the International Computer Science Institute.

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Michael Pecht is the Director of the CALCE Electronic Products and Systems Center at the University of Maryland and a Full Professor. Dr. Pecht has a BS in Acoustics, a MS in Electrical Engineering and a MS and PhD in Engineering Mechanics from the University of Wisconsin. He is a Professional Engineer, an IEEE Fellow and an ASME Fellow and a Westinghouse Fellow. He has written eleven books on electronics products development. He served as chief editor of the *IEEE Transactions on Reliability* for eight years and on the advisory board of *IEEE Spectrum*. He is currently the chief editor for *Microelectronics Reliability International*. He serves on the board of advisors for various companies and provides expertise in design, test and reliability assessment of electronics products and systems.

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E. Jan Vardaman received her B.A. in Economics and Business from Mercer University in 1979 and her M.A. in Economics from the University of Texas in 1981. She worked on the corporate staff of Microelectronics and Computer Technology Corporation in Austin, Texas where she analyzed international developments in software including artificial intelligence and semiconductor packaging and assembly. In 1987 she founded TechSearch International, Inc., providing licensing and consulting services in semiconductor packaging and is the president.

She is the editor of *Surface Mount Technology: Recent Japanese Developments*, published by IEEE. She is a columnist with *Circuits Assembly* magazine, and author of numerous publications on emerging trends in semiconductor packaging and assembly. She served on the NSF sponsored World Technology Evaluation Center (WTEC) study team involved in investigating electronics manufacturing in Asia. She is a member of

IEEE's CPMT society, IMAPS, and SMTA. In 1991 she received a technical contribution award for an outstanding technical lecture delivered at the SHM International Internecon Joint seminar held on January 22, 1991. She has served on the program committee for numerous IEEE conferences and workshops including the International Electronics Manufacturing Technology (IEMT) Symposium, the IEEE Computer Packaging Workshop in Santa Cruz, and the Electronics Components and Technology Conference (ECTC). She was the Vice Chair of the IEEE CPMT International Electronics Manufacturing Technology Symposium in 1995 and the General Chair in 1996. In 1997 she received an appreciation award for the many years of technical guidance and management provided to the IEMTS. She served as the U.S. liaison to the IEMT Japan conference from 1989 until its merger with IMC, where she continues her liaison duties. She served on the board of directors of IEPS and worked on the merger of IEPS and ISHM to create a combined society and received an appreciation award in 1997. She has been a course instructor at the ECTC since 1995.

Jan's specialty is analyzing international developments in the field of semiconductor packaging and assembly. She has been an invited speaker for IEMT/IMC in Japan, IMAPS/Nordic in Finland, Singapore, the 30th Anniversary of IMAPS France, and the opening of the Fraunhofer Institute in Berlin, Germany. She has authored more than 100 technical papers and made more than 90 presentations worldwide.